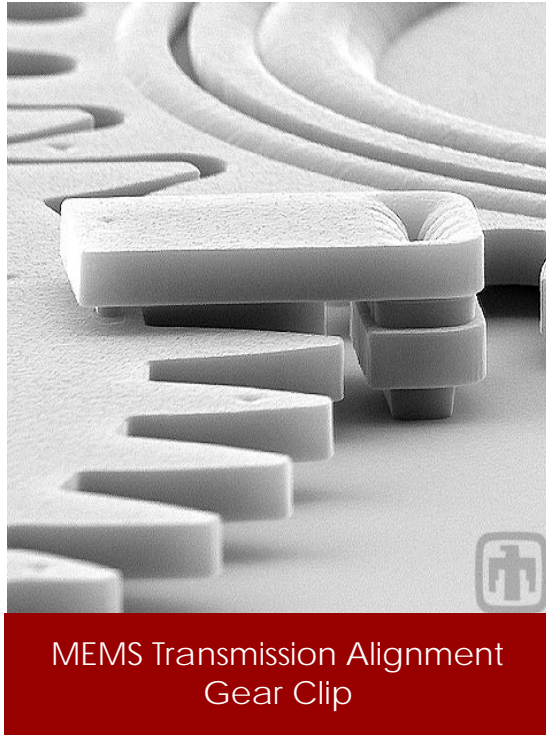


SVM Offers 200mm MEMS Processing

According to MEMS marketing experts, the MEMS market will double by the year 2012
-SANTA CLARA, Calif., July 29, 2009



MEMS Transmission Alignment
Gear Clip

“SVM is committed to assisting our customers in developing and scaling their current 150mm and 200mm MEMS processes to yield cost savings and economics of scale in today’s competitive landscape”

SVM’s President, Patrick Callinan, announced today that SVM has developed and will now offer 200mm MEMS processing. “SVM is committed to assisting our customers in developing and scaling their current 150mm and 200mm MEMS processes to yield cost savings and economies of scale in today’s competitive landscape”, said Callinan.

SVM’s strengths include prototyping and scaling of 100mm, 125mm, 150mm, and 200mm SOI/Si wafers. Options for processing include DRIE etch, wafer bonding, deposition, and photolithography for a single step and/or multiple steps in a flow. SVM has created solutions for MEMS devices such as microfluidic chips, sensors, and other related devices.

SVM’s MEMS processing capabilities include depositing low stress SiN film, building annular rings as small as 2-4 microns, patterning and resolving silicon channels to 100 nanometers, etching deep silicon reservoirs to depths of 1 micron, bonding silicon and glass materials, depositing metals (gold, platinum, etc) in glass, and glass drilling via lasers, milling, anisotropic etch methods and trenching as small and wide as 20 microns.

SVM also offers silicon substrates in thicknesses of 0.05mm to 1mm with well-controlled surface roughness and flatness. These include SOI wafers, soft borosilicate glass, fused silica, natural quartz, magnetic materials, metals, and dielectrics.

Silicon Valley Microelectronics (SVM), founded in 1990, is the largest distributor of silicon wafers and services in the world. SVM maintains an extensive inventory of wafers with a variety of specifications in all diameters and offers an array of wafer processing capabilities ranging from thermal oxide to lithography.

For more information on SVM’s MEMS processing, please visit www.svmi.com.